



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

the patent application of

Hathaway et al.

Serial No.:

10/709327

Group Art Unit: Unknown

Filing Date:

4-28-04

Examiner: Unknown

For: METHOD OF IDENTIFYING PATHS WITH DELAYS DOMINATED BY A PARTICULAR FACTOR

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicants' duty of disclosure under 37 CFR §1.56, applicants respectfully bring the following documents, listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicants are aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 09-0456.

Respectfully submitted,

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A circular stamp from the Patent & Trademark Office. The text "PATENT & TRADEMARK OFFICE" is written along the bottom inner edge. The date "JUL 27 2004" is stamped in the center. At the top, the number "0428" is handwritten. The words "U.S. DEPARTMENT OF COMMERCE" are partially visible at the very top.

~~Also several sheets if necessary)~~

BUR9200300176US1

SERIAL NO.

10/709327

Hathaway et al.

FILING

GROUP

Unknown

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

			W.E. Donath, E.M. Sharp, M.L. White, "Performance Evaluation For Automated Layout Procedures", IBM Technical Disclosure Bulletin, Vol. 25, No. 10, March 1983, Pages 5049-5051
			J.W. Davis, R.D. Glenn, G. Lleonart, "Optimum Routing of Critical Circuit Timing Paths", IBM Technical Disclosure Bulletin, Vol. 30, No. 12, May 1988, Pages 300-302

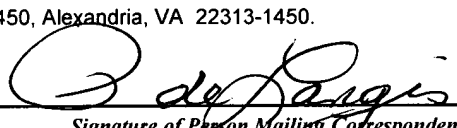
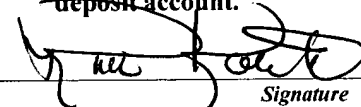
EXAMINER

DATE CONSIDERED

***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) and 1.97(c))		Docket No. BUR920030176US1	
In Re Application Of: Hathaway et al.			
Serial No. 10/709327	Filing Date 4-28-04	Examiner Unassigned	Group Art Unit Unassigned
Title: METHOD OF IDENTIFYING PATHS WITH DELAYS DOMINATED BY A PARTICULAR FACTOR			
<p>Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450</p> <p>37 CFR 1.97(b)</p> <p>1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.</p> <p>37 CFR 1.97(c)</p> <p>2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:</p> <p><input type="checkbox"/> the statement specified in 37 CFR 1.97(e);</p> <p>OR</p> <p><input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).</p>			

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. BUR920030176US1
In Re Application: Hathaway et al.			
Serial No. 10/709327	Filing Date 4-28-04	Examiner Unassigned	Group Art Unit Unassigned
METHOD OF IDENTIFYING PATHS WITH DELAYS DOMINATED BY A PARTICULAR FACTOR			
<p style="text-align: center;">Payment of Fee</p> <p style="text-align: center;">(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))</p> <p> <input type="checkbox"/> A check in the amount of _____ is attached. </p> <p> <input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. 090456 as described below. </p> <div style="margin-left: 40px;"> <input type="checkbox"/> Charge the amount of _____ <input checked="" type="checkbox"/> Credit any overpayment. <input checked="" type="checkbox"/> Charge any additional fee required. </div>			
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<p>*This certificate may only be used if paying by deposit account.</p> <div style="display: flex; justify-content: space-between;"> <div style="width: 45%;">  Richard M. Kotulak, Reg. # 27712 IP Law Department, 972E IBM Corporation 1000 River Street Essex Junction, VT 05452 Telephone: 802-769-4457 </div> <div style="width: 45%;"> Dated: July 29, 2004 </div> </div>			
CC:			